

# *Ionic Migration on Printed-Circuit Boards*

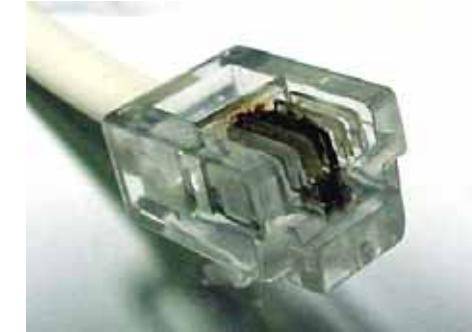
ESPEC CORP.

## CONTENS

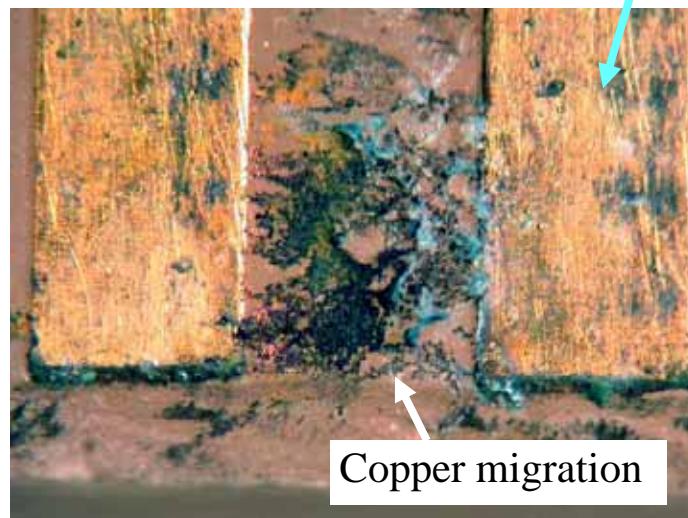
1. *Introduction*
2. *About ionic migration*
3. *Test methods*
4. *Insulation resistance test*  
*(Copper and silver migration)*
5. *Solder alloy migration*

## *Migration occurring on electronics components*

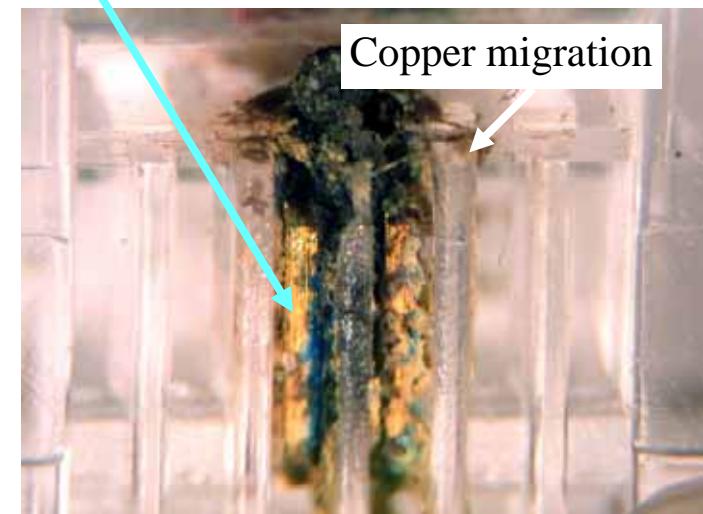
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Gold plated copper terminal



Copper migration

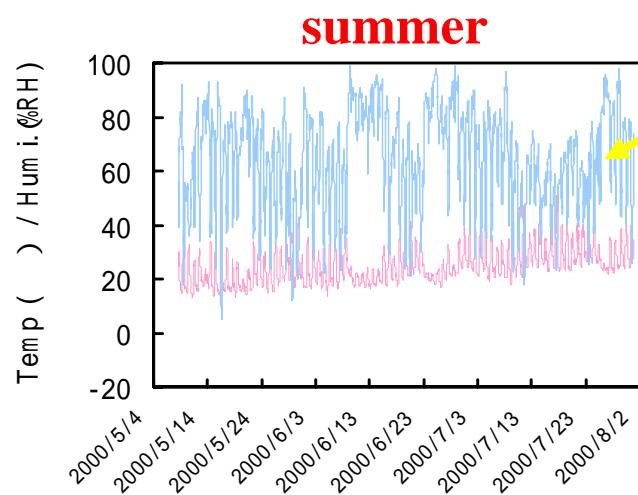
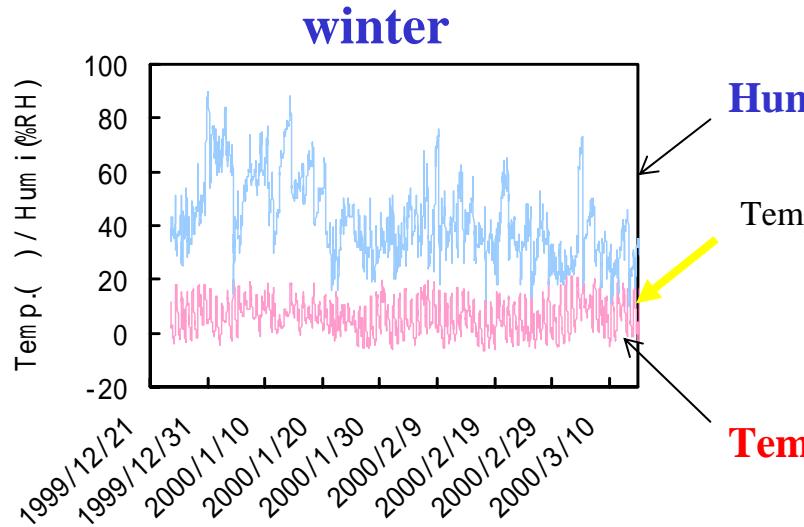


Copper migration

(a) Copper migration on PCB

(b) Copper migration on telephone connector

# *Failure on PCB and environmental stress*

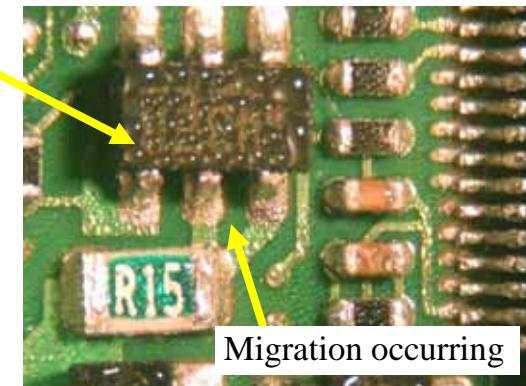


(a) *Temperature and humidity in automobile*

## **Connected deterioration**

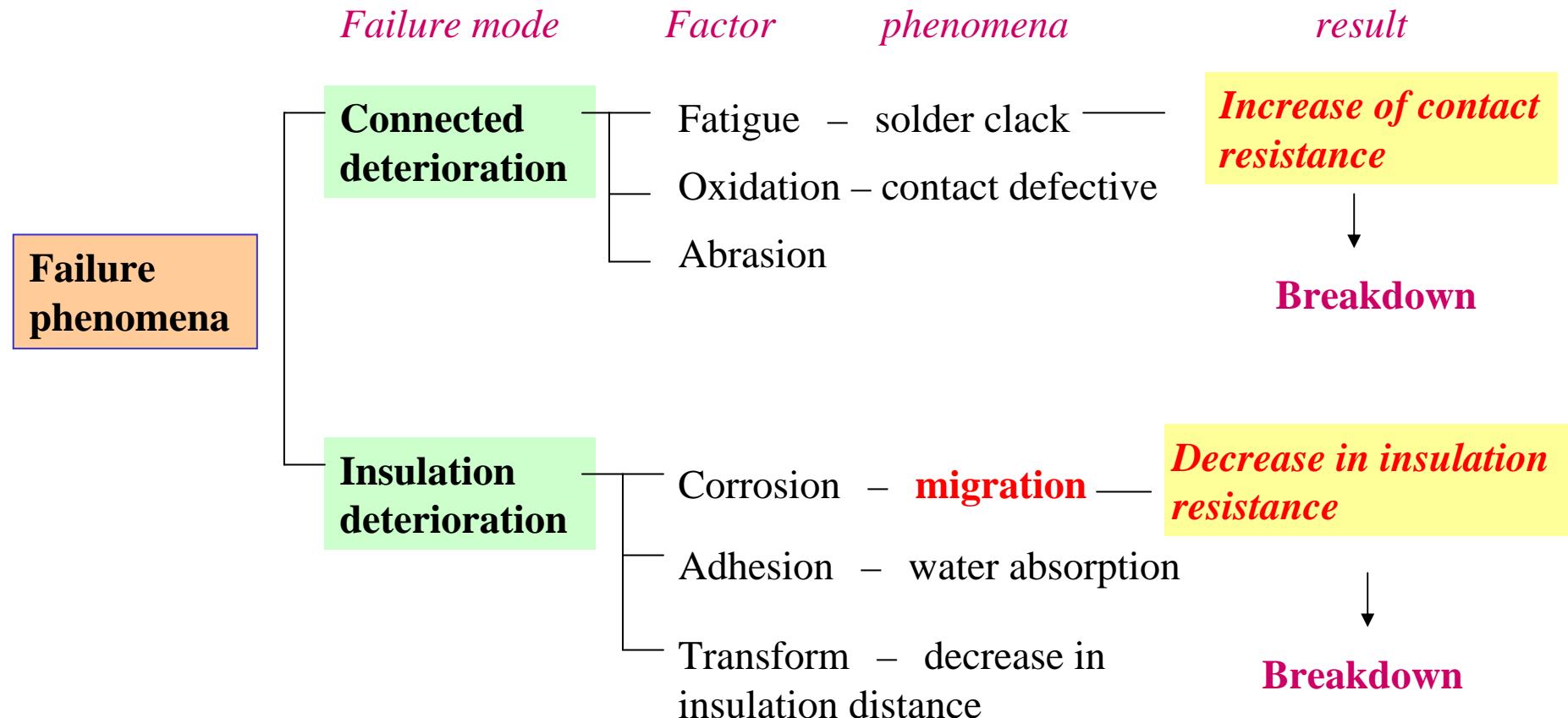


## **Insulation deterioration**



(b) *Failure on PCBs*

# *Factors of failure on PCB*

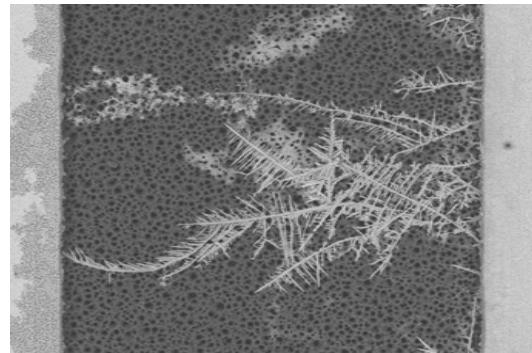


## *About ionic migration*

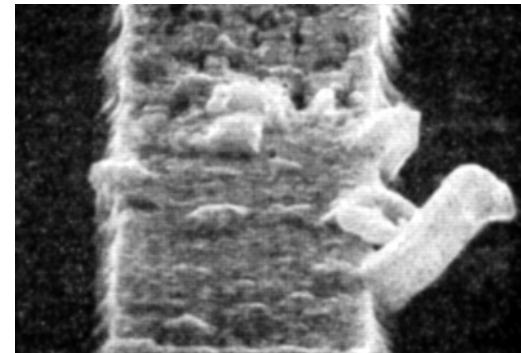
# *Classification of migration phenomenon*

Reaction	Classification	Phenomenon	Failure part
Electrochemistry	Ionic migration (electrochemical migration)	The metal ionizes, a metallic ion migrates by the electric field.	Between wiring for PCBs
Physical	Electro migration	Interaction of metallic atom and electron	Aluminum wiring of semiconductor
	Stress migration	A metallic atom migrates by the mechanical stress.	
	Thermal migration	A metallic atom migrates by the thermal stress.	

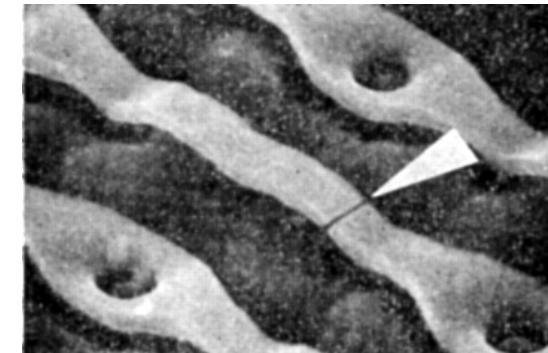
(a) Ionic migration  
(electrochemical migration)



(b) Electro migration



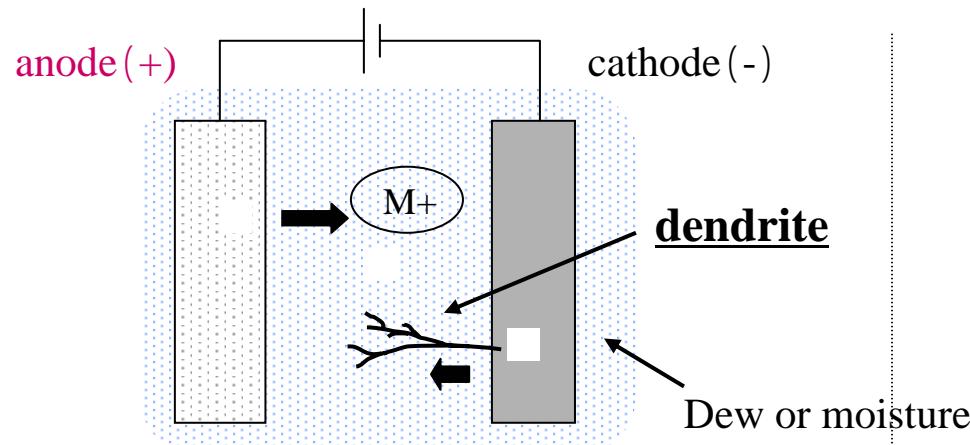
(c) Stress migration



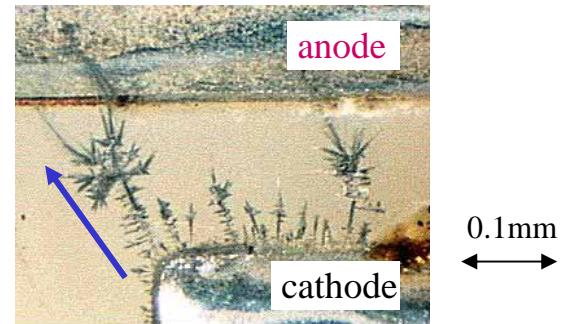
Reference (b) (c ): Tsuneo Ajiki: "reliability of semiconductor device", Nikkagiren, 1988

# *The forms of Ionic Migration*

**(a) Dendrite**

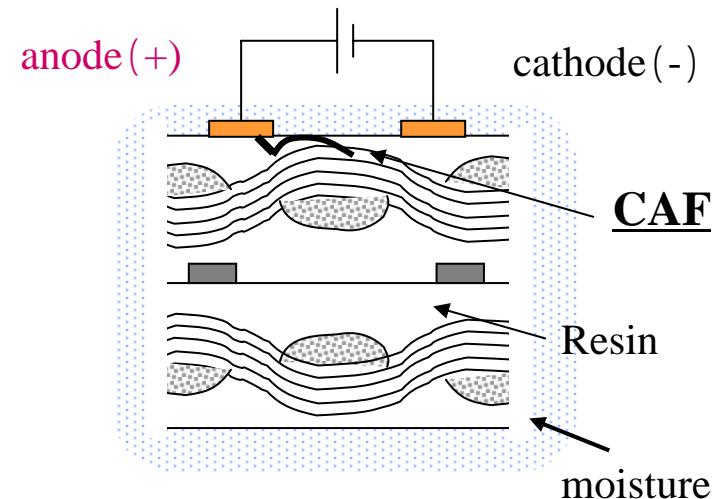


Phase1: metallic dissolution  
Phase2: metallic ion migrate  
Phase3: metallic deposition

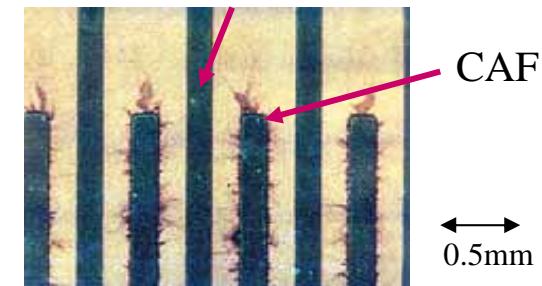


**Solder dendrite on PCB**

**(b) CAF (Conductive Anodic Filament)**

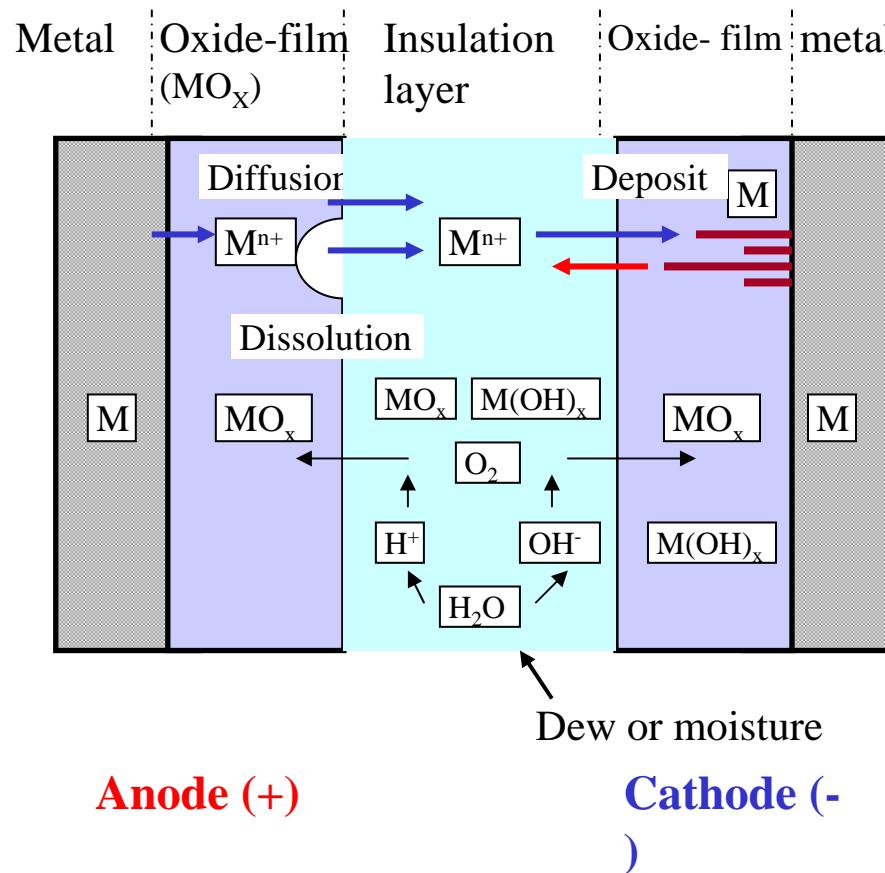


Copper electrode

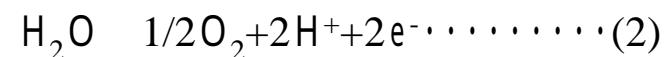


**Copper CAF in PCB**

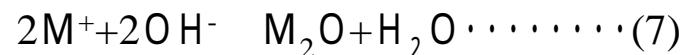
# *Reaction mechanism of Ionic Migration*



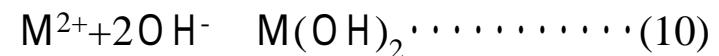
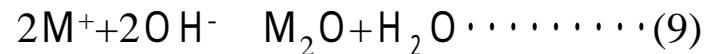
## Anodic reaction



## Cathodic reaction



## Reaction between electrodes

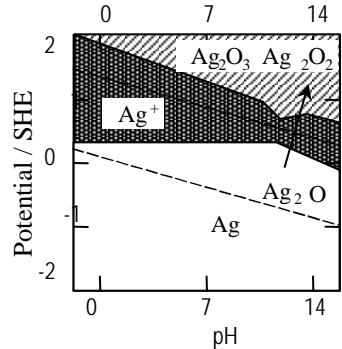


## *Acceleration factor of Ionic Migration*

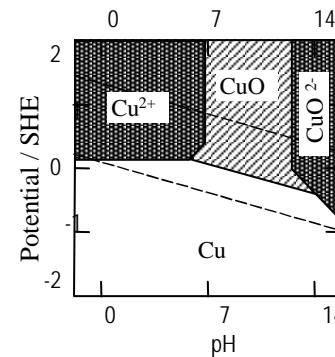
Factor	Acceleration condition
Materials	( Fast) Ag > Cu > Pb > Sn-Pb Solder > Sn > Au ( Slow)
Temperature	High Temp
Humidity	High humidity
Voltage	High voltage
pH	Acidity
Ionic impurities	Halogen material (Chlorine, Bromine)
Printed-cuircuit board material	Paper phenol > Glass epoxy > Polyimide > Ceramic

## *Acceleration factor : materials (Pourbaix potential - pH diagrams)*

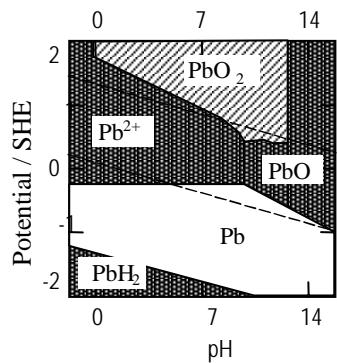
Silver (Ag)



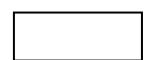
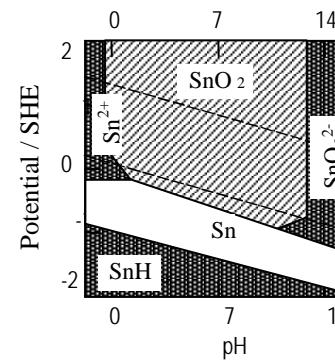
Copper (Cu)



Lead (Pb)



Tin (Sn)



immunity



passivity



corrosion

## *Acceleration factor : materials (Energy of oxide - film)*

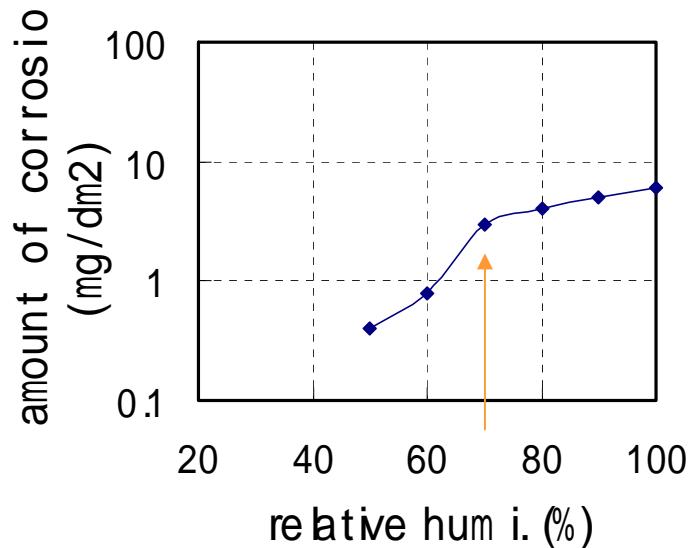
Gibbs free energy change of metal oxide-film

metal oxide-film	$Gf^\circ$ ( kJ/ mol )
SnO <sub>2</sub>	-515.47
Bi <sub>2</sub> O <sub>3</sub>	-496.64
Pb <sub>2</sub> O <sub>3</sub>	-321.92
ZnO	-321.92
SnO	-257.32
PbO	-189.33
CuO	-127.19
Ag <sub>2</sub> O	-10.75

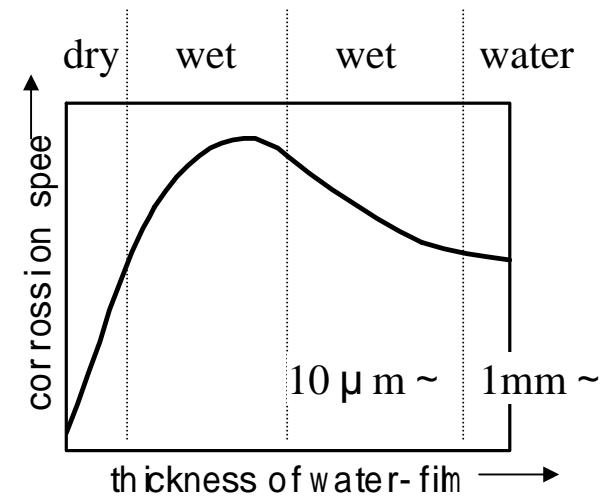
Stability



## *Acceleration factor : Humidity (water-film and corrosion)*



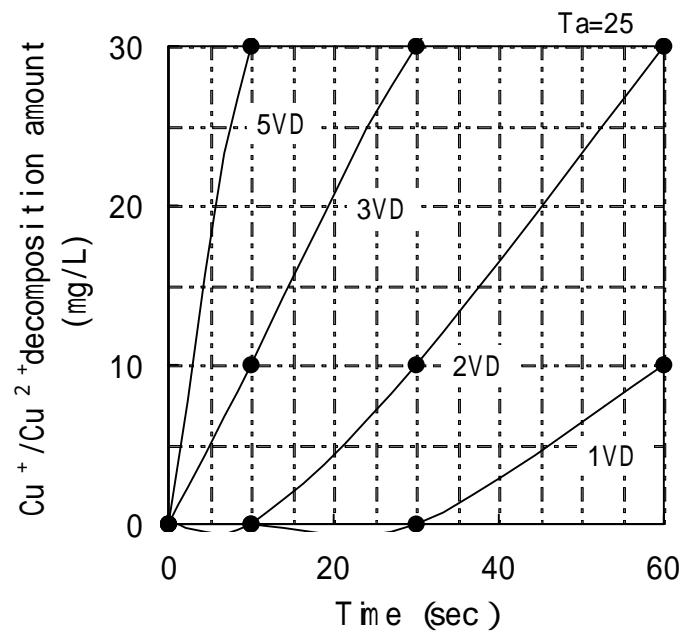
(a) Relative humidity and corrosion of metal(iron)



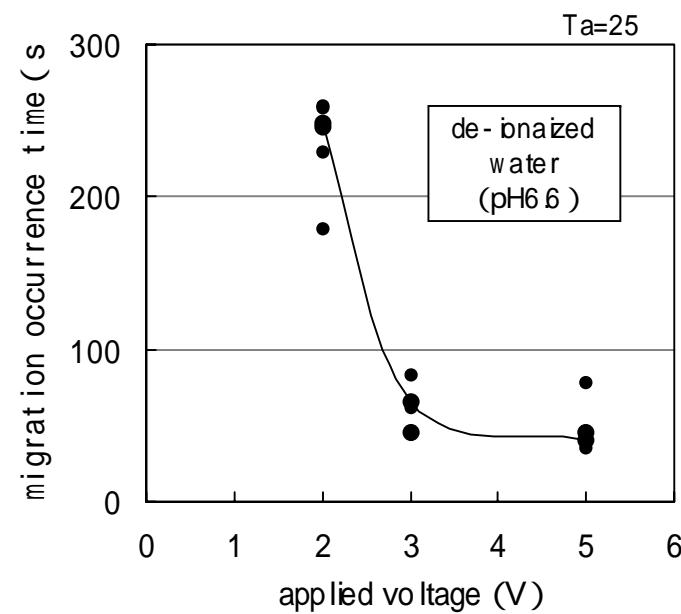
(b) Thickness of water-film and corrosion speed

Reference (a): varon, w. : Trans, faraday soc., 23, 113(1927)  
(b): Tomashov, N D.: corros., 20, 7t(1964)

## *Acceleration factor : Voltage*



(a) Quantitative characteristics of elution: voltage and copper ions



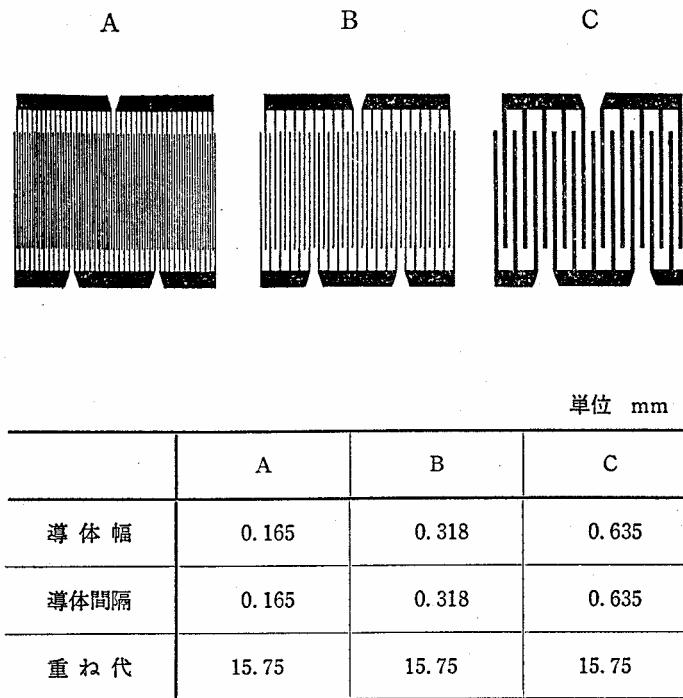
(b) The relationship between applied voltage and migration occurrence

## *Test methods*

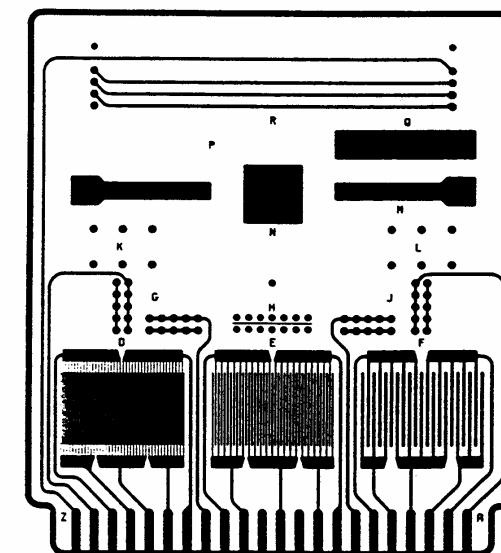
## Test standards for Insulation evaluation

Standard	Test name	Test conditions	Applied voltage	Measurement Voltage
IPC-TM-650-2.6.13	Assessment of Susceptibility to Metallic Dendritic Growth Uncoated Printed Wiring	25 De-ionized water: 60mL	0 ~ 20V / DC (MAX 15mA)	
ANSI-J-STD-004	Requirement for Soldering Fluxes	85 , 85%RH , 168H	50V / DC	100V / DC
JIS-Z-3284	Solder Paste	40 , 90-95%RH , 1000H	45 to 50V / DC	100V / DC
		85 , 85 to 90%RH , 1000H	45 to 50V / DC	100V / DC
IPC-TM-650-2.6.3	Moisture and Insulation Resistance of printed boards	35 , 85 to 93%RH , 4days	100V / DC	Decided in consultation with purchaser
		50 , 85 to 93%RH , 7days	100V / DC	

## *Migration test pattern on PCB*

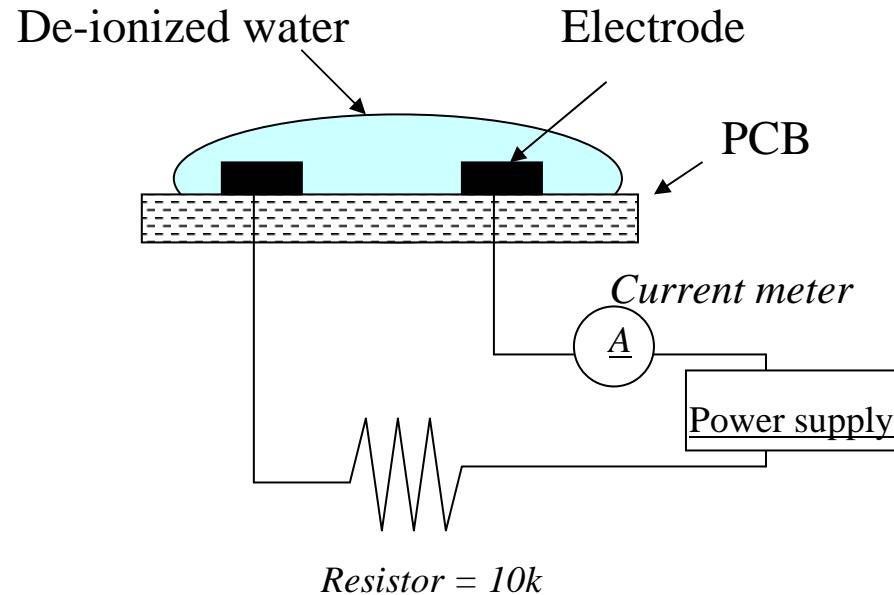


(a) IPC test pattern

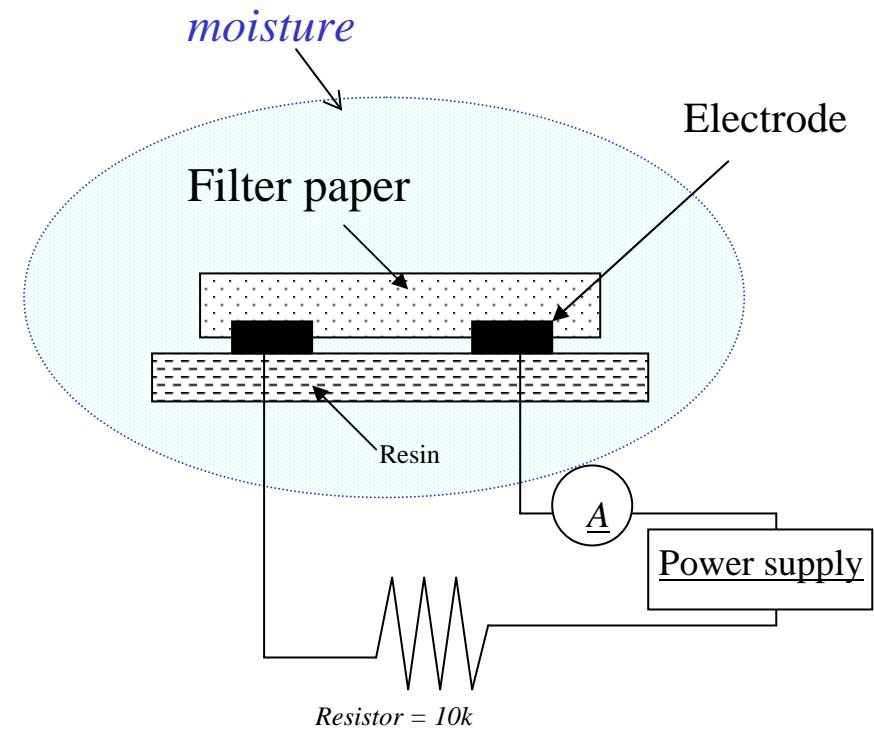


(b) IPC multi-purpose test board

## *Test methods : Simple migration test*



(a) Water drop test



(b) Filter paper test

## *Test methods : Environmental migration test*

Temperature / humidity chamber



Dew condensation cyclic chamber



HAST



(a) Evaluation by absorption

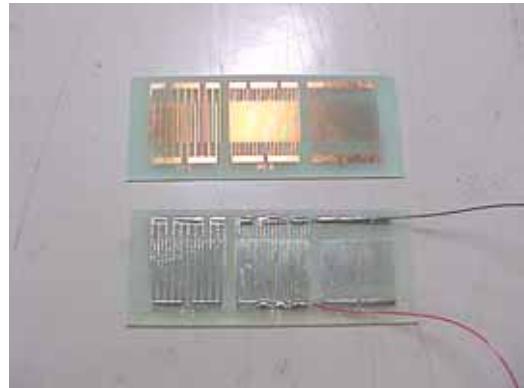
Temperature / humidity cyclic chamber



(b) Evaluation by dew

*measurement method : Manual measurement*

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(1) specimen



(3) Insulation resistance meter (HP4329)



(2) High Temp/High Humi. test



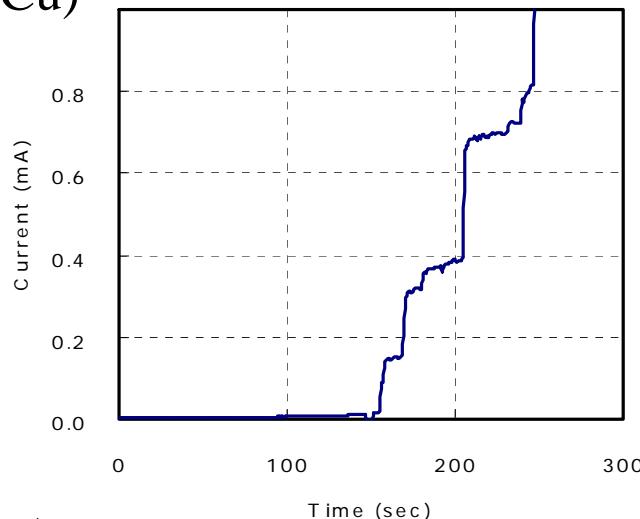
(4) Insulation resistance meter (HP4339)

## *Insulation resistance test of PCBs*

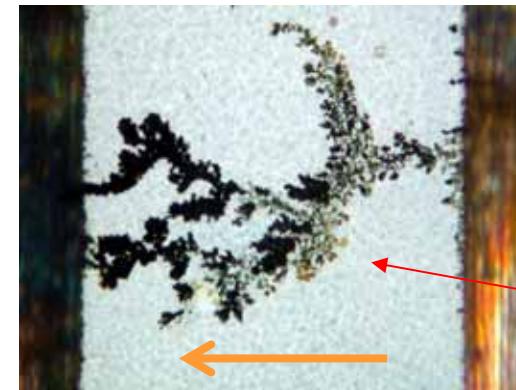
## *Results of Water Drop test (Cu and Ag, 2V/DC bias)*

Copper (Cu)

*Change in current*

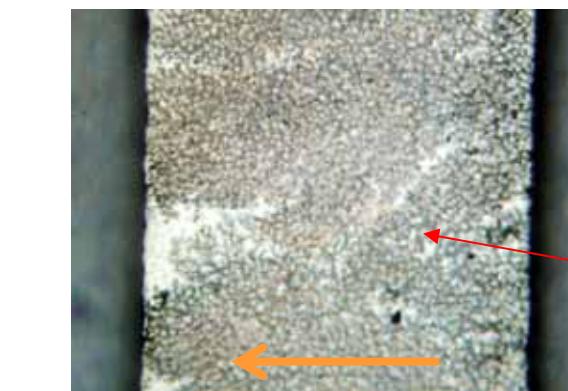
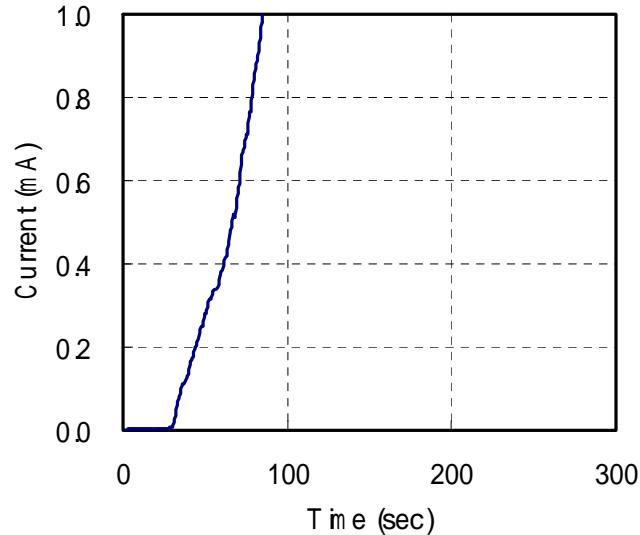


*External appearance after testing*



Copper  
migration

Silver (Ag)



Silver  
migration

anode (+)

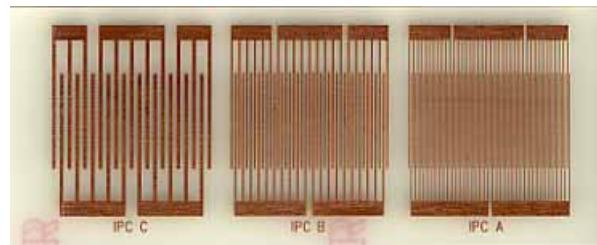
cathode (-)

## *Surface insulation resistance (SIR) test for PCB*

(a) Test condition

Test conditions	40 ,87% 60 ,87% 85 ,85% 1000Hr
PCB material	Glass-cloth epoxy (FR-4) IPC-B comb pattern (Gap=0.3mm)
Applied voltage	50V / DC
Measurement intervals	1 Hr

(b) Specimen



Grass-epoxy PCB

Ionic migration evaluation system (ESPEC)

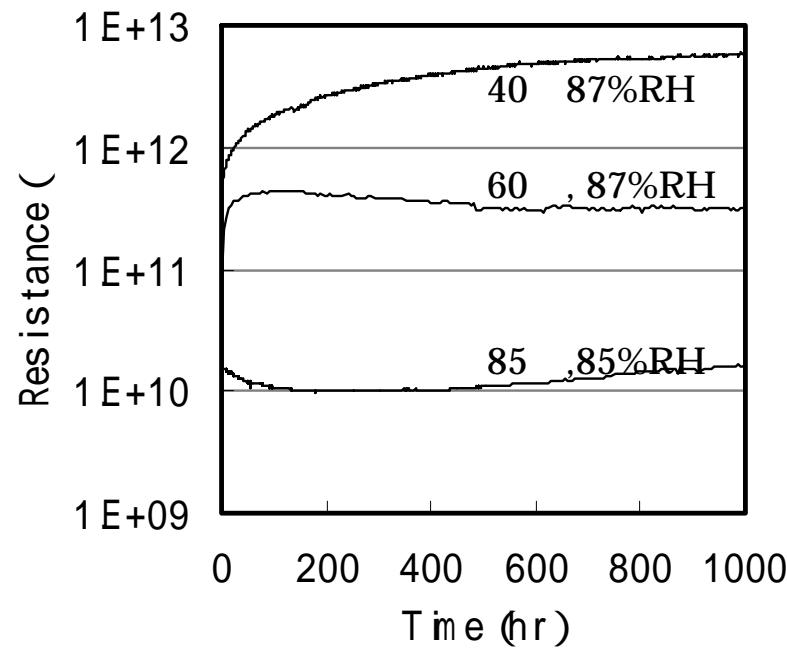


Wiring situation

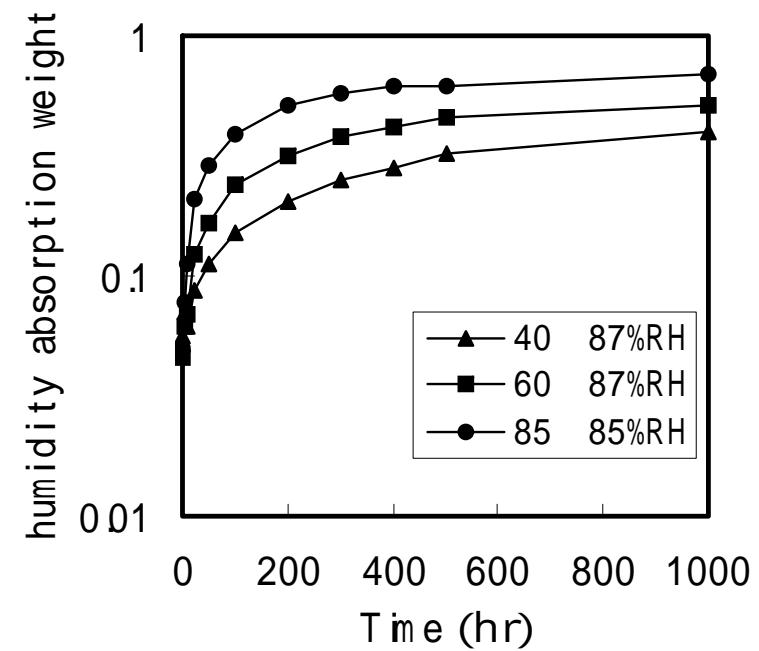


## *Results of SIR test (Copper pattern PCB, 50V/DC bias)*

*Change in insulation resistance*



*Change in absorption characteristic*

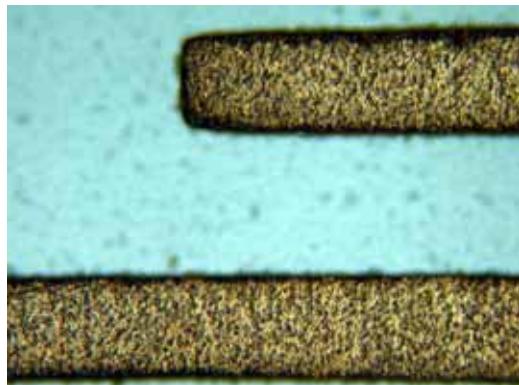


## *Results of SIR test (Copper pattern PCB, 50V/DC bias)*

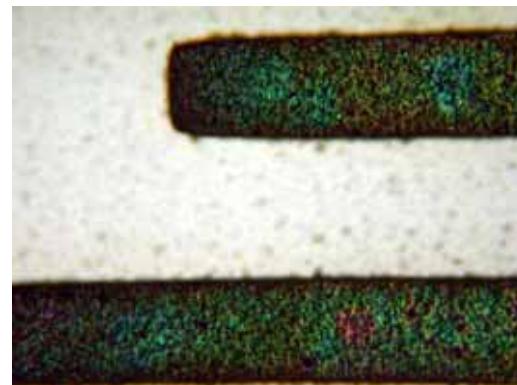
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External appearance after testing (2000Hour)

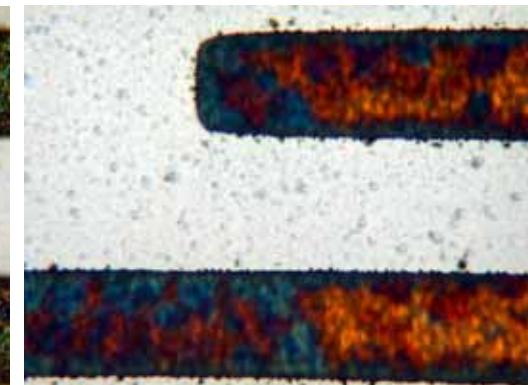
**40** , 87%



**60** , 87%



**85** , 85%



anode (+)

No migration

No migration

No migration

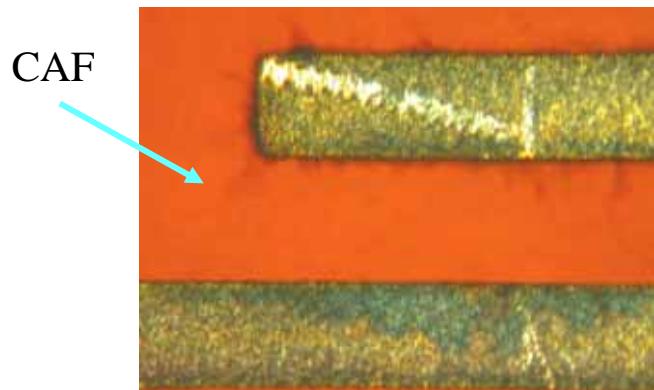
cathode (-)

*Example : Results of SIR test (Paper phenolic PCB)*

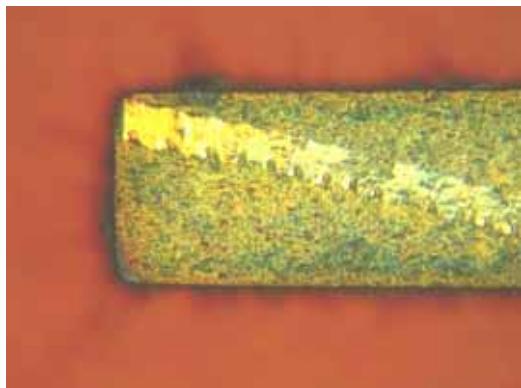
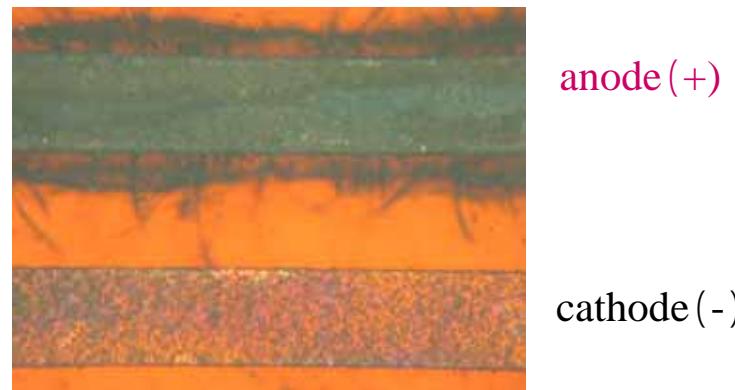
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External appearance after testing

85 , 85%, 50V/DC, 2000Hr



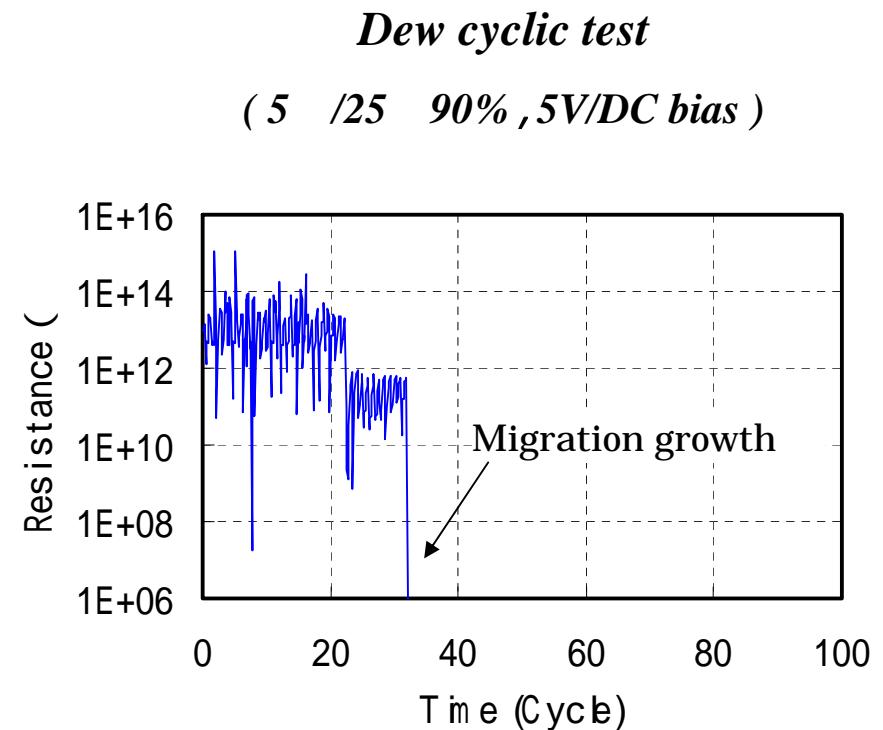
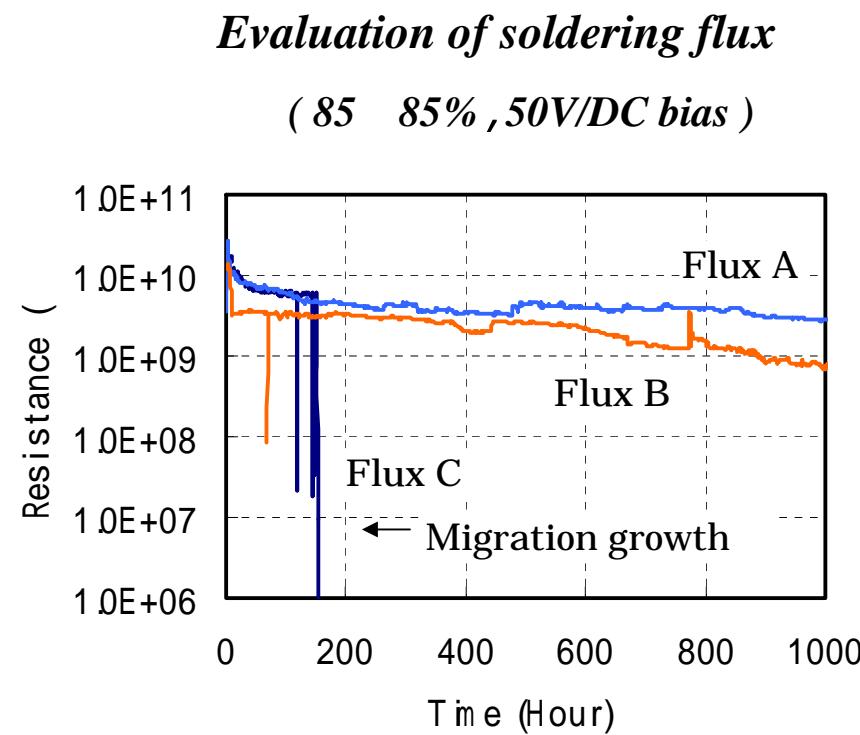
110 , 85%, 5V/DC, 300Hr



CAF growth

CAF growth

## *Example : Results of SIR test (Flux and Dew test)*



## *Life estimated formula of insulation failure*

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IPC-9201 (Surface Insulation Resistance Handbook)

$$t2 = t1 \times \exp\left[\left(\frac{Ea}{R}\right)\left(\frac{1}{T1} - \frac{1}{T2}\right)\right] \times \exp\left[b\left(\frac{1}{RH1} - \frac{1}{RH2}\right)\right] \times \left(\frac{A2V2}{A1V1}\right)$$

The Institute of Electrical Engineers of Japan (Report No. 772)

$$AF = \exp\left(\frac{Ea}{R}\left(\frac{1}{T1} - \frac{1}{T2}\right)\right) \times \left(\frac{RH2}{RH1}\right)^r \times \left(\frac{V2}{V1}\right)^n \times \left(\frac{D1}{D2}\right)^m$$

Ea = Activation energy ( Glass Epoxy PCB = 1eV)

R = Boltzmann's Constant ( $8.63 \times 10^{-5}$  eV/k)

T1, RH1, V1, D1, t2 = Practice use Temp., Humi., Voltage, Electrode Distance, Time

T2, RH2, V2 , D2, t1= Accreted test Temp., Humi., Voltage, Electrode Distance, Time

B, A1,A2, r, n, m = constant ( r=3, n=2)

## *Life estimated formula of insulation failure*

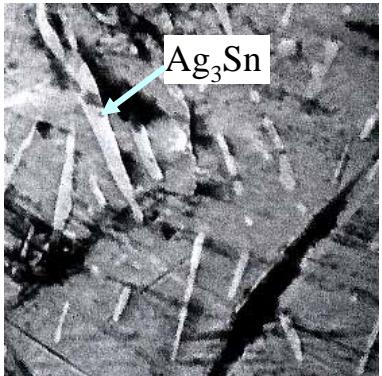
$$AF = \exp\left(\frac{Ea}{R}\left(\frac{1}{T1} - \frac{1}{T2}\right)\right) \times \left(\frac{RH2}{RH1}\right)^r \times \left(\frac{V2}{V1}\right)^n \times \left(\frac{D1}{D2}\right)^m$$

- $Ea$  = Activation energy  
(Glass Epoxy PCB = 1eV)
- $R$  = Boltzmann's Constant ( $8.63 \times 10^{-5}$  eV/k)
- Practice Temp.(T1)=45 ,  
Humi.(RH1)=85%
- Test Temp(T2)=85 , Humi.(RH2)=85%
- $r, n$  = constant ( r=3, n=2)
- $AF1 = \exp[1.16 \times 10^4 \times (1/318 - 1/358)] = \exp(4.1) = 59$
- $AF2 = (85/85)^3 = 1$
- $AF3 = (5/5)^2 = 1$
- $AF = 59 \times 1 \times 1 = \underline{\underline{59}}$
- Voltage(V1)= 5V
- Voltage(V2)=5V, 2000Hr
- Life Time =  $2000\text{Hr} \times AF = 118000\text{Hr} = \underline{\underline{13.4 \text{ years}}}$

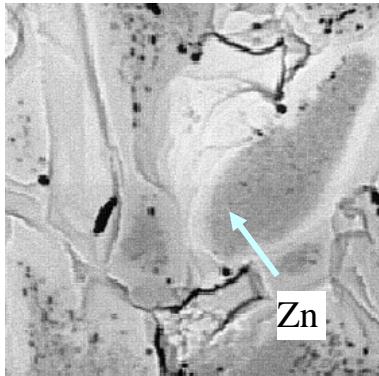
# *Solder alloy migration*

# Factors of Solder alloy migration

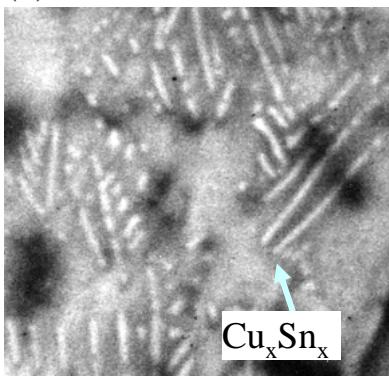
(a) Sn-3.5Ag



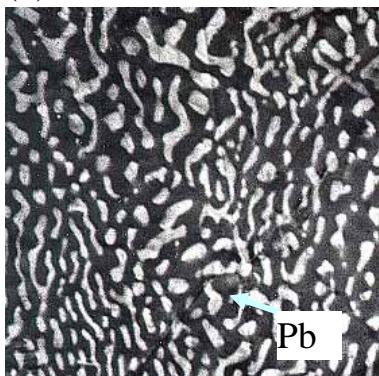
(c) Sn-9Zn



(b) Sn-0.75Cu



(d) Sn-37Pb



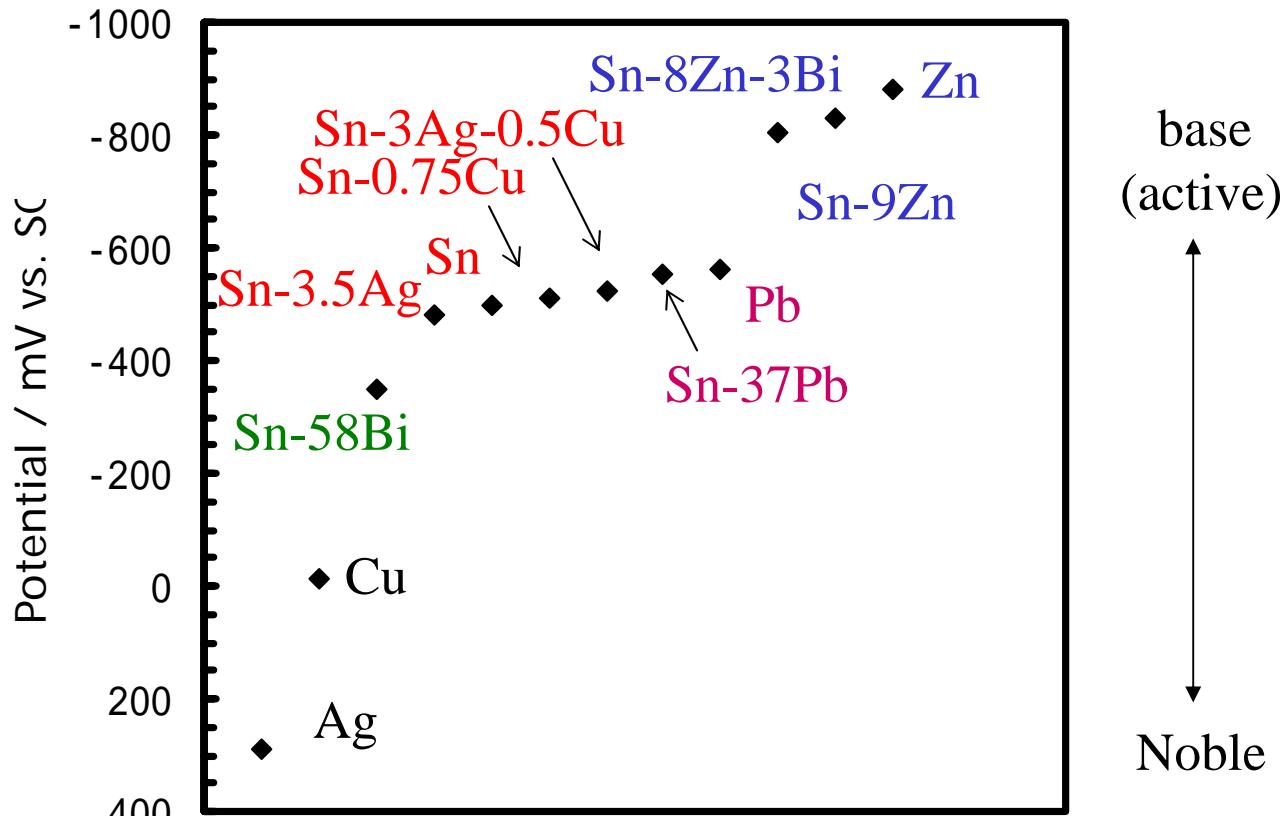
$\longleftrightarrow$   
20 μm

(a) SEM image of solder surfaces

materials	Reaction	$E^\circ$ (V vs. SHE)	
base	Zinc	$\text{Zn}^{2+} + 2\text{e}^- = \text{Zn}$	-0.763
	Tin	$\text{Sn}^{2+} + 2\text{e}^- = \text{Sn}$	-0.138
	Lead	$\text{Pb}^{2+} + 2\text{e}^- = \text{Pb}$	-0.126
	Hydrogen	$2\text{H}^+ + 2\text{e}^- = \text{H}_2$	0.000
	Bismuth	$\text{Bi}^{3+} + 3\text{e}^- = \text{Bi}$	0.215
	Copper	$\text{Cu}^{2+} + 2\text{e}^- = \text{Cu}$	0.337
Noble	Silber	$\text{Ag}^+ + \text{e}^- = \text{Ag}$	0.779

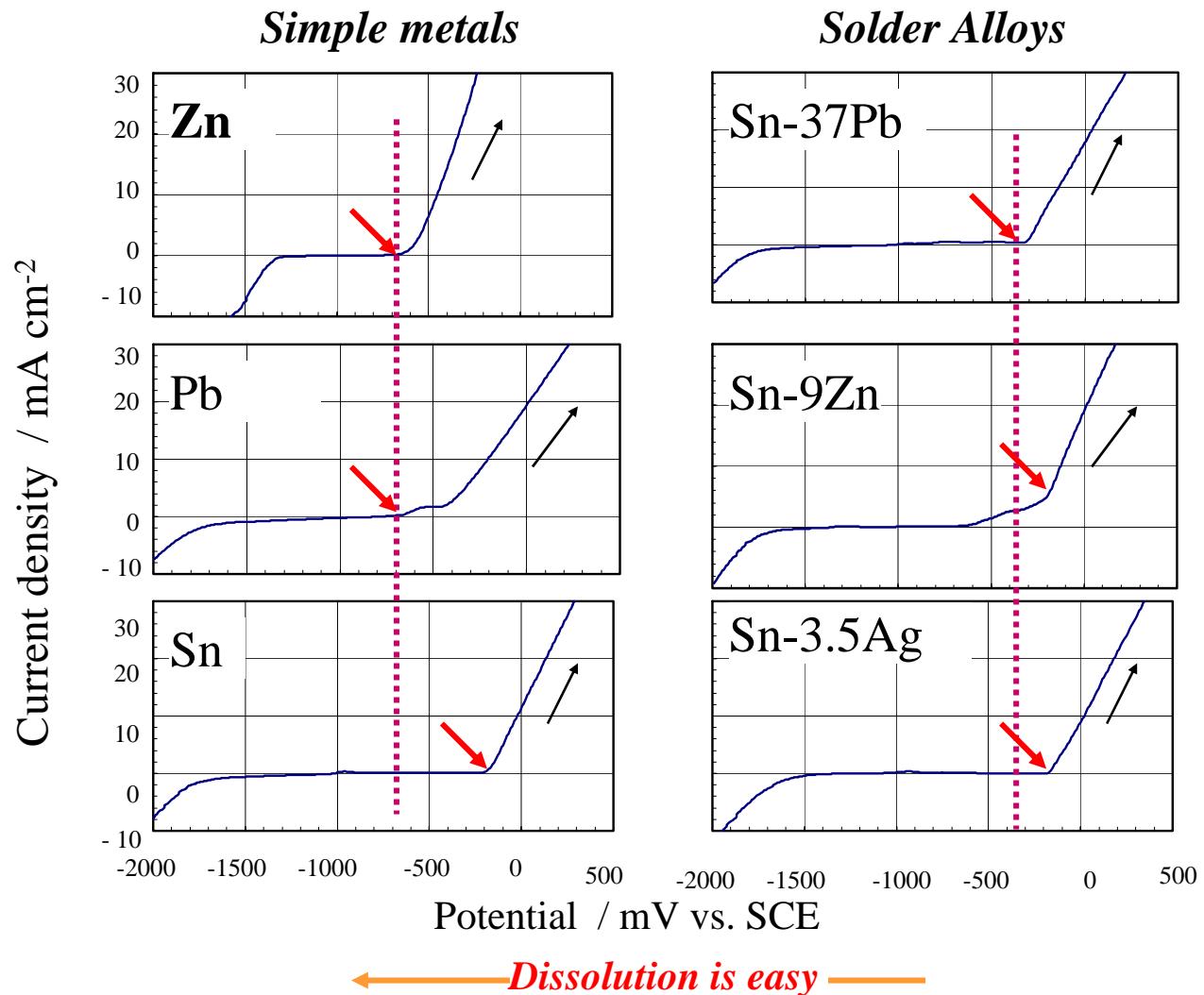
(b) Standard potential of solder composition materials

## *Electrochemical characteristics (Static characteristic)*



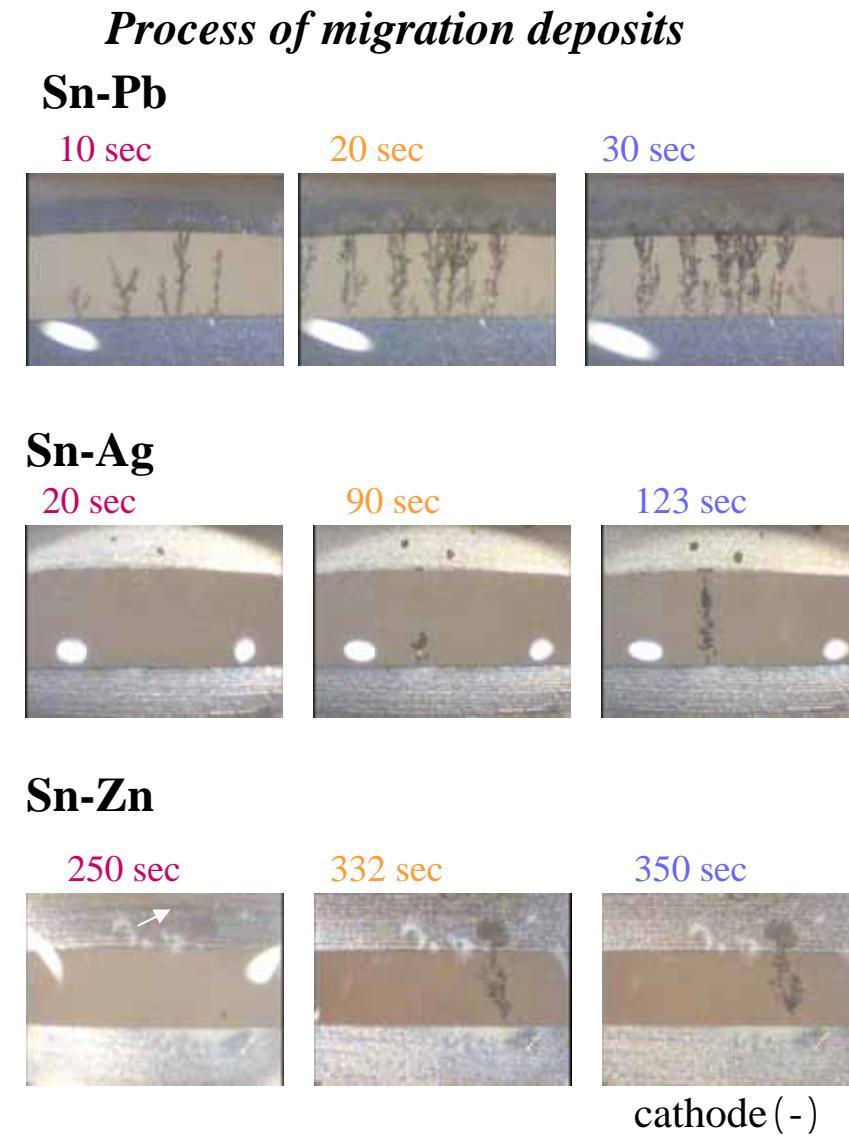
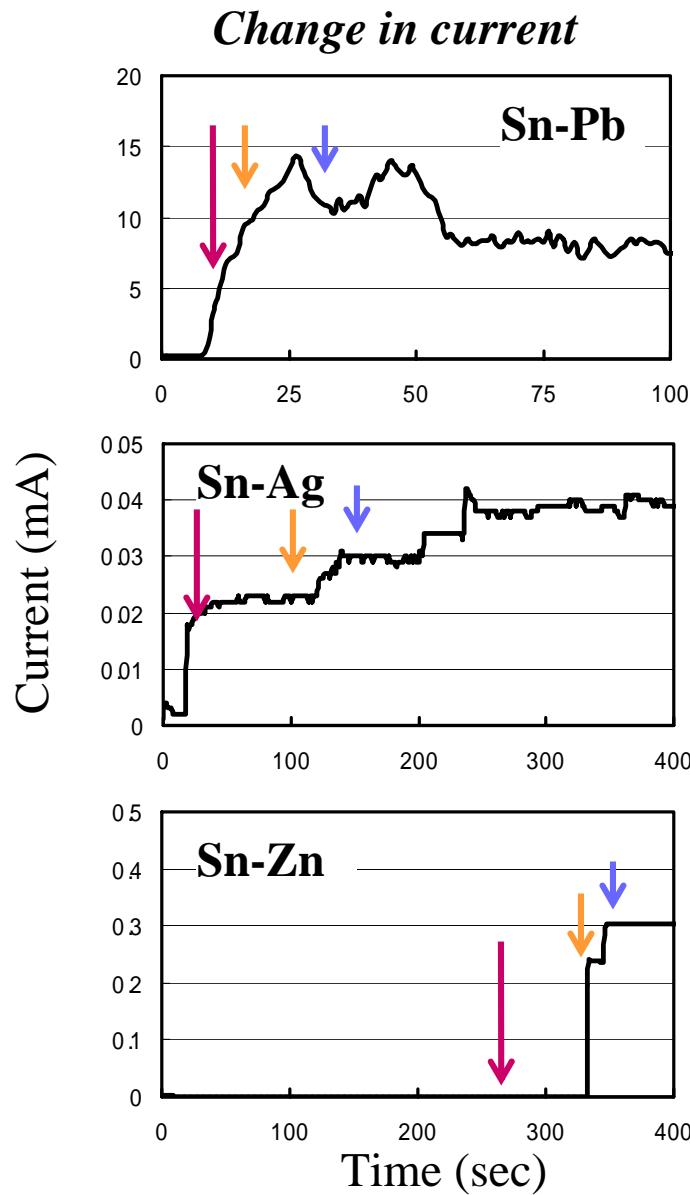
Rest potential of each type solder (in 0.1M KO<sub>3</sub> aqueous solution)

## *Electrochemical characteristics (Dynamic characteristic)*



Current - potential curves = dissolution characteristic (in 0.1M KO<sub>3</sub> aqueous solution)

## *Water Drop test - 1 (5V/DC bias)*

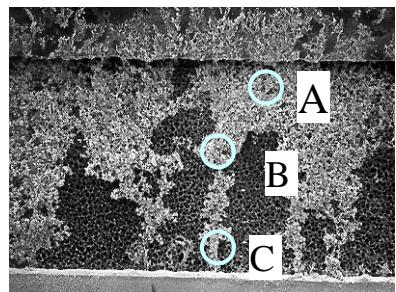


## *Water Drop test - 2 (SEM analysis)*

***SEM image of migration deposits***

**Sn-Pb**

anode (+)

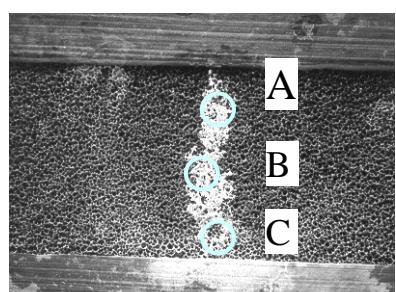


***Compositional analysis of migration deposits***

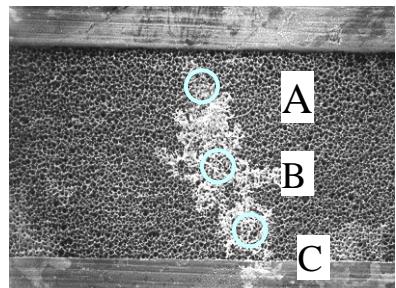
solder	Point A	Point B	Point C
Sn-Pb	Sn = 63% Pb = 37%	Sn = 46% Pb = 54%	Sn = 47% Pb = 53%
Sn-Ag	Sn = 100% Ag = 0%	Sn = 100% Ag = 0%	Sn = 100% Ag = 0%
Sn-Zn	Sn = 98% Zn = 2%	Sn = 93% Zn = 7%	Sn = 97% Zn = 3%

**Sn-Ag**

cathode (-)



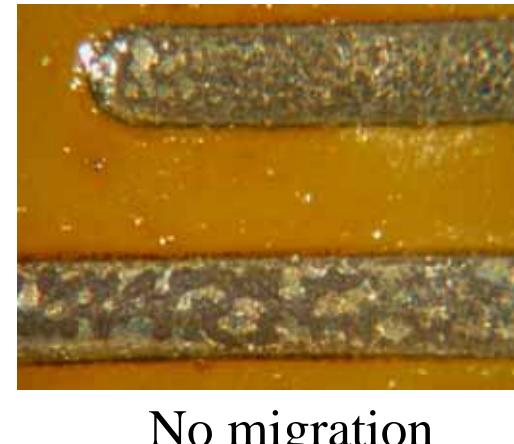
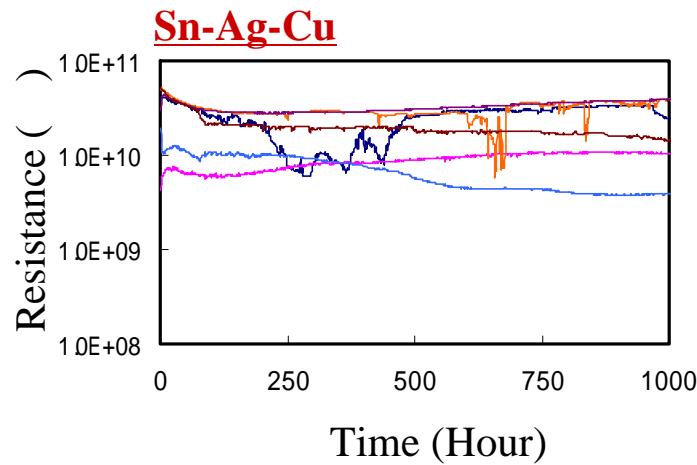
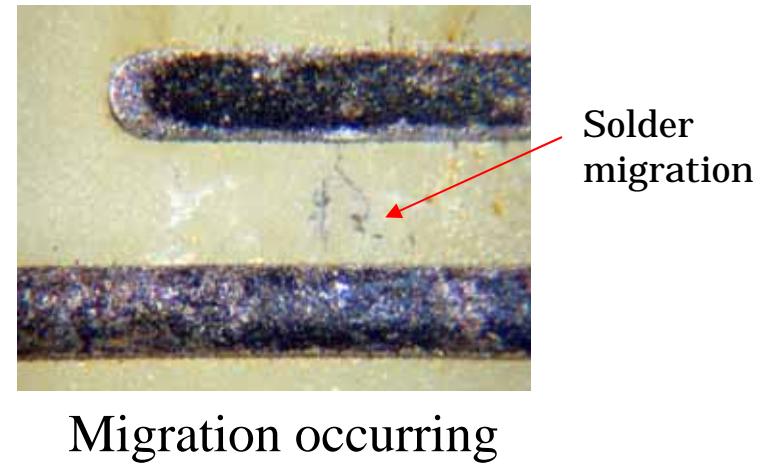
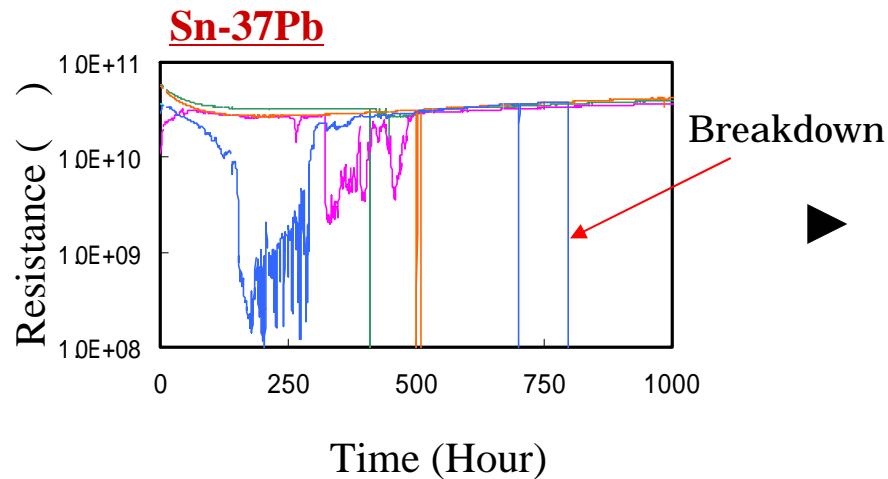
**Sn-Zn**



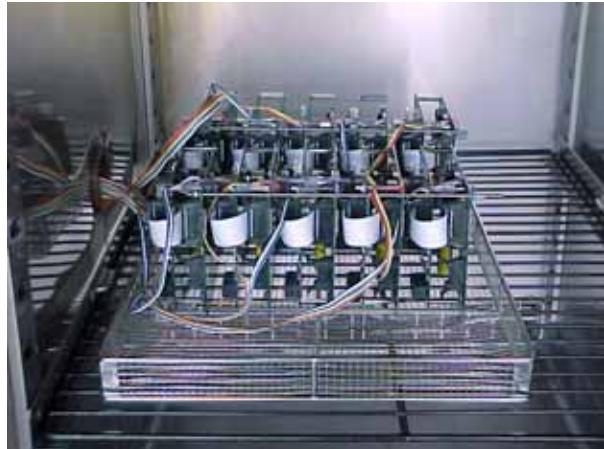
↔  
200  $\mu$  m

## *Surface insulation resistance test (50V/DC bias, 85 °C 85%)*

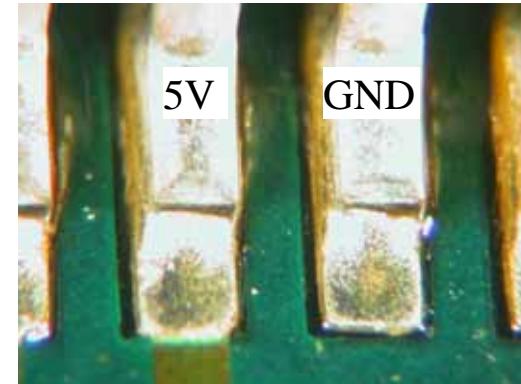
### *Change in insulation resistance*



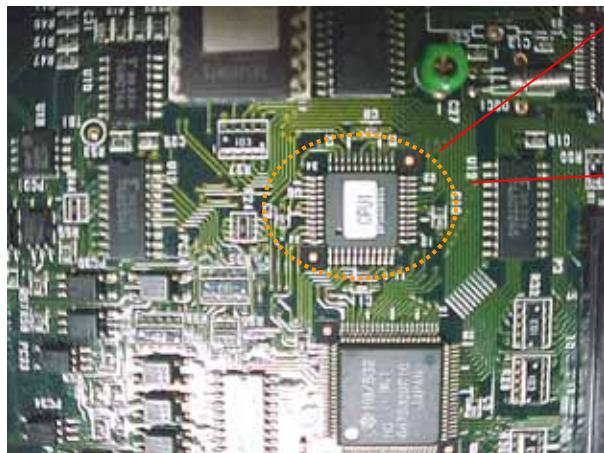
## *Evaluation using mounting PCBs (THB Testing, 5/DC bias , 80 90%)*



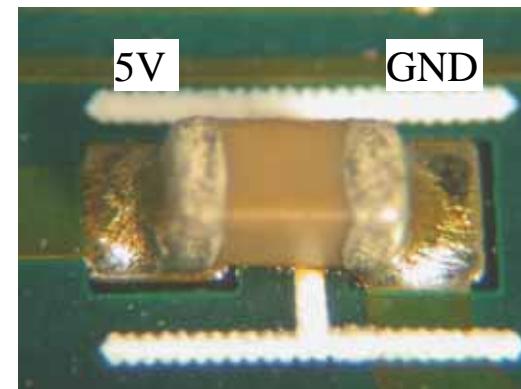
Terminal of QFP (spacing:0.5mm)



No migration



Chip capacitance (spacing:0.8mm)



No migration

Sn-Ag-Cu solder

END